

FIG. 1

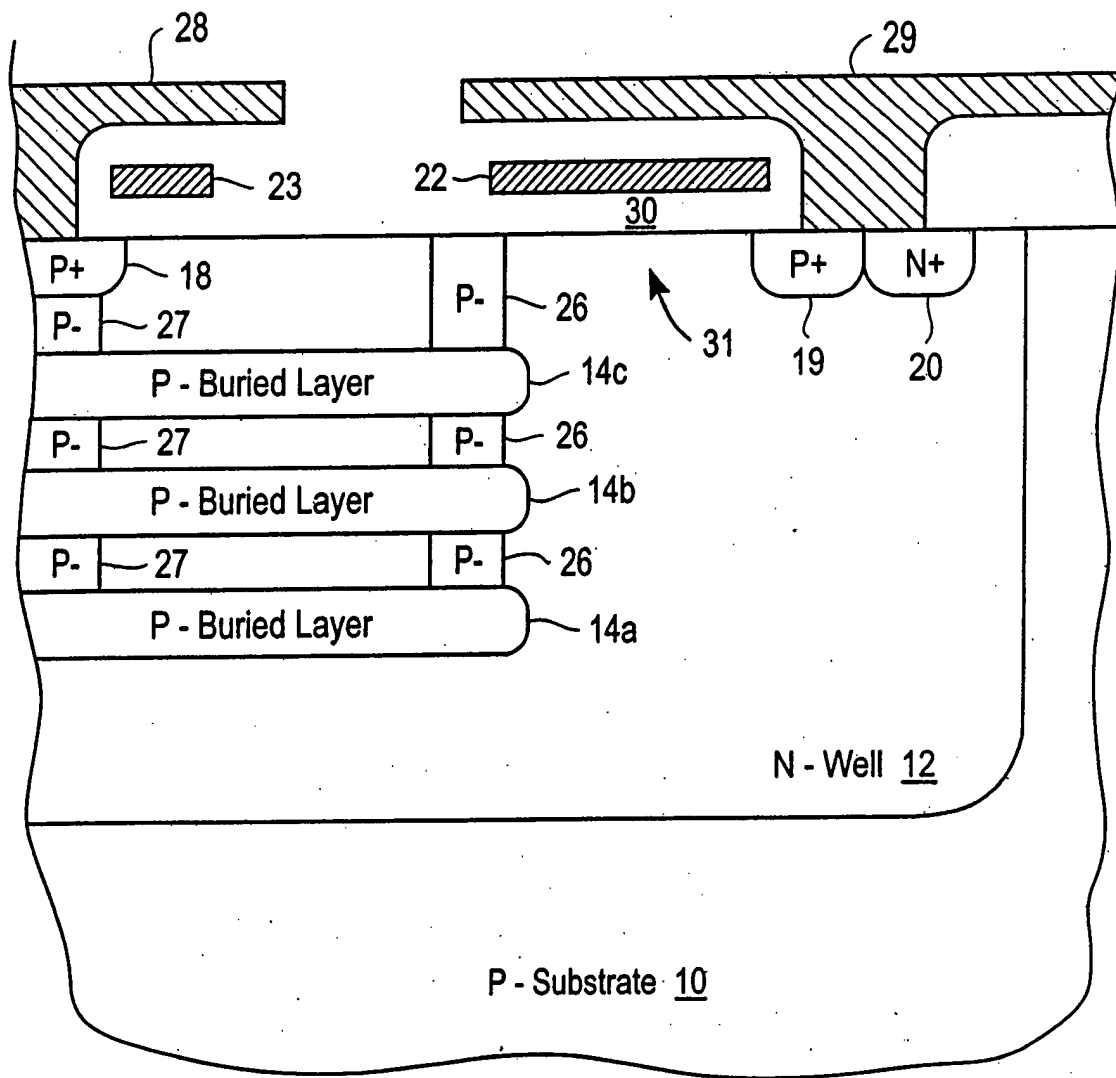
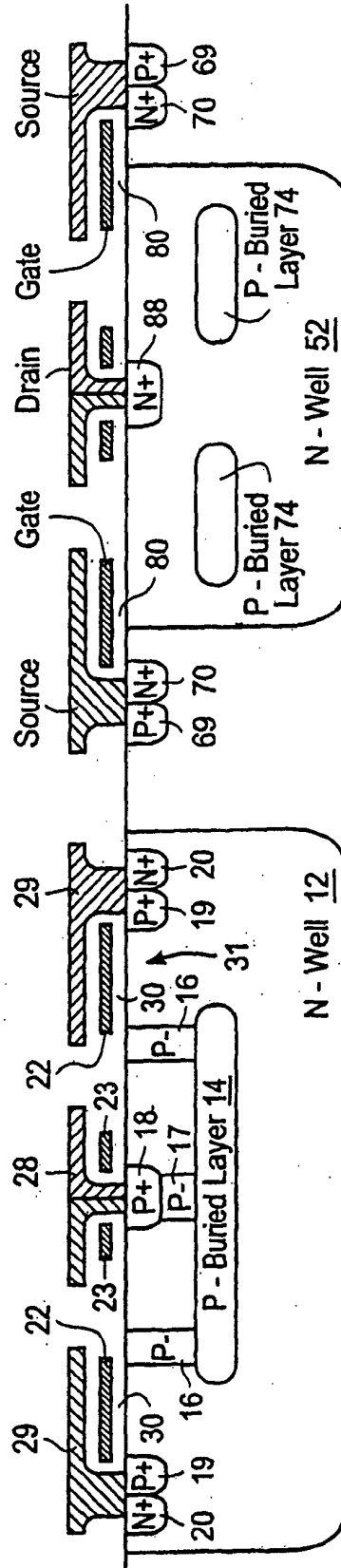


FIG. 2

HV NMOS



N - Well 52

FIG. 3